

Texas Instruments Incorporated

Post Office Box 655474, MS 3999
Dallas, Texas 75265
7839 Churchill Way
Dallas, Texas 75251

RECEIVED
CENTRAL FAX CENTER

NOV 09 2004

FAX COVER SHEET

DATE: 11/9/04

Page 1 of 5

FROM: ROBERT MARSHALL	TO: EXAM. DAVE HULISMAN
COMPANY NAME: Robin Barnum Texas Instruments Incorporated	COMPANY NAME:
PHONE NUMBER: (972) 917-5296 // 917-4374	PHONE NUMBER: 571-272-4168
FAX NUMBER: (972) 917-4418	FAX NUMBER: 703-872-9306

MESSAGE

TI-30559

S/N 09/702,320

DAVE, Per our phone conversation earlier today, attached are copies of the RCE, Fax PC, TI Internal Fax Acknow., and PTO Auto Reply Fax Acknow. Since you never received the first sent RCE.

Robin Barnum
for Robert D. Marshall Jr.

RECEIVED
CENTRAL FAX CENTER

NOV 09 2004

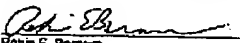
Auto-Reply Facsimile Transmission



TO: Fax Sender at 972 917 4418
Fax Information
Date Received: 5/3/2004 10:45:45 AM [Eastern Daylight Time]
Total Pages: 2 (including cover page)

ADVISORY: This is an automatically generated return receipt confirmation of the facsimile transmission received by the Office. Please check to make sure that the number of pages listed as received in Total Pages above matches what was intended to be sent. Applicants are advised to retain this receipt in the unlikely event that proof of this facsimile transmission is necessary. Applicants are also advised to use the certificate of facsimile transmission procedures set forth in 37 CFR 1.8(a) and (b), 37 CFR 1.6(f). Trademark Applicants, also see the Trademark Manual of Examining Procedure (TMEP) section 306 et seq.

Received
Cover
Page
=====>

NOV-03-2004 09:39 FACD6133		972 917 4418 P.01																
To:	Technology Center: 2183 Facsimile Number: 703-872-9306	Total Pages Sent: 2																
From:	Robert D. Marshall, Jr. Texas Instruments Incorporated Facsimile: 972-917-4418 Phone: 972-917-5290																	
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE																		
In re the Application of	Laurence R. Sinar, Jr., et al.	TI-30559																
Serial No.:	09/702,320	Art Unit: 2183																
Filed:	October 31, 2000	Examiner: David J. Huisman																
For:	Microprocessor With Execution Packet Spanning Two or More Fetch Packets	Conf. No.: 9784																
CERTIFICATION OF FACSIMILE TRANSMISSION																		
I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent and Trademark Office at 703-872-9306 on the date shown below:																		
 Robin E. Barnum		Date: May 3, 2004																
FACSIMILE COVER SHEET																		
<table border="1"><tr><td><input checked="" type="checkbox"/> FACSIMILE COVER SHEET</td><td><input type="checkbox"/> AMENDMENT (X of Pages)</td></tr><tr><td><input type="checkbox"/> NEW APPLICATION</td><td><input type="checkbox"/> EOT (X of Pages)</td></tr><tr><td><input type="checkbox"/> DECLARATION (X of Pages)</td><td><input type="checkbox"/> NOTICE OF APPEAL (X of Pages)</td></tr><tr><td><input type="checkbox"/> ASSIGNMENT (X of Pages)</td><td><input type="checkbox"/> APPEAL (X of Pages)</td></tr><tr><td><input type="checkbox"/> FORMAL DRAWINGS</td><td><input type="checkbox"/> INITIAL FDC (X of Pages)</td></tr><tr><td><input type="checkbox"/> INFORMAL DRAWINGS</td><td><input type="checkbox"/> REPLY BRIEF (MULTIPLE) (X of Pages)</td></tr><tr><td><input type="checkbox"/> CONTINUATION (X of Pages)</td><td><input checked="" type="checkbox"/> Request for Continued Examination (RCE) (1 page)</td></tr><tr><td><input type="checkbox"/> DIVISIONAL APPN</td><td></td></tr></table>			<input checked="" type="checkbox"/> FACSIMILE COVER SHEET	<input type="checkbox"/> AMENDMENT (X of Pages)	<input type="checkbox"/> NEW APPLICATION	<input type="checkbox"/> EOT (X of Pages)	<input type="checkbox"/> DECLARATION (X of Pages)	<input type="checkbox"/> NOTICE OF APPEAL (X of Pages)	<input type="checkbox"/> ASSIGNMENT (X of Pages)	<input type="checkbox"/> APPEAL (X of Pages)	<input type="checkbox"/> FORMAL DRAWINGS	<input type="checkbox"/> INITIAL FDC (X of Pages)	<input type="checkbox"/> INFORMAL DRAWINGS	<input type="checkbox"/> REPLY BRIEF (MULTIPLE) (X of Pages)	<input type="checkbox"/> CONTINUATION (X of Pages)	<input checked="" type="checkbox"/> Request for Continued Examination (RCE) (1 page)	<input type="checkbox"/> DIVISIONAL APPN	
<input checked="" type="checkbox"/> FACSIMILE COVER SHEET	<input type="checkbox"/> AMENDMENT (X of Pages)																	
<input type="checkbox"/> NEW APPLICATION	<input type="checkbox"/> EOT (X of Pages)																	
<input type="checkbox"/> DECLARATION (X of Pages)	<input type="checkbox"/> NOTICE OF APPEAL (X of Pages)																	
<input type="checkbox"/> ASSIGNMENT (X of Pages)	<input type="checkbox"/> APPEAL (X of Pages)																	
<input type="checkbox"/> FORMAL DRAWINGS	<input type="checkbox"/> INITIAL FDC (X of Pages)																	
<input type="checkbox"/> INFORMAL DRAWINGS	<input type="checkbox"/> REPLY BRIEF (MULTIPLE) (X of Pages)																	
<input type="checkbox"/> CONTINUATION (X of Pages)	<input checked="" type="checkbox"/> Request for Continued Examination (RCE) (1 page)																	
<input type="checkbox"/> DIVISIONAL APPN																		
NAME OF INVENTOR: Laurence R. Sinar, Jr., et al.		Serial No.: 09/702,320																
TITLE OF INVENTION: Microprocessor With Execution Packet Spanning Two or More Fetch Packets		Filing Date: October 31, 2000																
CLASSIFICATION: TI-30559	CLASSIFICATION: 20-0468																	
PARTIAL: 5404	DUE: 5/19/04																	
ATTENTION: RCE/Inv																		
<small>This form is to be used only for the use of the address named and contains legally privileged and/or confidential information. If you are not the authorized recipient of this message, you are hereby notified that any disclosure, distribution, copying or use of this communication is strictly prohibited. Applicable provisions are not intended by virtue of the document having been transmitted by facsimile. Any funds received in connection with this message should be returned to the sender by mail to the address indicated on this cover sheet.</small>																		
<small>Texas Instruments Incorporated PO Box 555414, M/S 9699 Dallas, TX 75255</small>																		
PAGE 10 * RCVD AT 5/3/2004 11:55:13 AM (Session Expires: 11:55:13) * FACD6133 * FAX 972 917 4418 * DURATION (mm:ss): 02:12																		

***** COMM. ... RNAL - ***** DATE MAY-03-2004 TIME 09:39 *****

MODE = MEMORY TRANSMISSION

START-MAY-03 09:38

END-MAY-03 09:39

FILE NO.=992

STN NO.	COMM.	ONE-TOUCH/ ABBR NO.	STATION NAME/EMAIL ADDRESS/TELEPHONE NO.	PAGES	DURATION
001	OK	2	817038729306-038026359	002/002	00:01:05

-FPCD6133-

***** -972 917 4418 - ***** 972 917 4418- *****

To: Technology Center: 2183
Facsimile Number: 703-872-9306
Total Pages Sent: 2

From: Robert D. Marshall, Jr.
Texas Instruments Incorporated
Facsimile: 972-917-4418
Phone: 972-917-5290

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of	TI-30559
Laurence R. Simar, Jr., et al.	Art Unit: 2183
Serial No.: 09/702,320	Examiner: David J. Hulsman
Filed: October 31, 2000	Conf. No.: 9784
For: Microprocessor With Execution Packet Spanning Two or More Fetch Packets	

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent and Trademark Office at 703-872-9306 on the date shown below:


Robin E. Bamum

May 3, 2004
Date

FACSIMILE COVER SHEET

<input checked="" type="checkbox"/> FACSIMILE COVER SHEET	<input type="checkbox"/> AMENDMENT <input checked="" type="checkbox"/> (2 Pages)
<input type="checkbox"/> NEW APPLICATION	<input type="checkbox"/> POT <input type="checkbox"/> (2 Pages)
<input type="checkbox"/> DECLARATION (# Pages)	<input type="checkbox"/> NOTICE OF APPEAL (# Pages)
<input type="checkbox"/> ASSIGNMENT (# Pages)	<input type="checkbox"/> APPEAL <input type="checkbox"/> (2 Pages)
<input type="checkbox"/> FORMAL DRAWINGS	<input type="checkbox"/> ISSUE FEE (# Pages)
<input type="checkbox"/> INFORMAL DRAWINGS	<input type="checkbox"/> REPLY BRIEF (IN TRIPLICATE) (# Pages)
<input type="checkbox"/> CONTINUATION APPN (# Pages)	<input checked="" type="checkbox"/> Request for Continued Examination (RCE) (1 page)
<input type="checkbox"/> DIVISIONAL APPN	
NAME OF INVENTOR(S): Laurence R. Simar, Jr., et al.	
TITLE OF INVENTION: Microprocessor With Execution Packet Spanning Two or More Fetch Packets	
TX FR NO: TI-30559	DEPOSIT ACCE NO: 28-0668
FAXED: 5/3/04	
DUE: 5/19/04	
ATTY/SECY: RDM/reb	
Serial No.: 09/702,320	
Filing Date: October 31, 2000	

This facsimile is intended only for the use of the address named and contains legally privileged and/or confidential information. If you are not the intended recipient of this telecopy, you are hereby notified that any dissemination, distribution, copying or use of this communication is strictly prohibited. Applicable privileges are not waived by virtue of the document having been transmitted by facsimile. Any misdirected facsimiles should be returned to the sender by mail at the address indicated on this cover sheet.

Texas Instruments Incorporated
PO Box 65474, M/S 3999
Dallas, TX 75265